

Executive Summary

In contemporary power and signal environments - industrial plants, transportation platforms, defense systems, medical devices, and renewable energy - the role of electromagnetic interference (EMI) filters has expanded beyond regulatory emissions and immunity compliance. Designers now face repeated exposure to electrical transients: lightning-induced surges, electrostatic discharge (ESD), inductive switching spikes, load-dump events, line sags/swells, network instabilities, and even an Electromagnetic Pulse from the high-altitude detonation of a nuclear weapon (HEMP). Under these conditions, two distinct design philosophies have emerged:

- 1. Traditional EMI Filters** with transient survivability are engineered to withstand transient exposure without suffering damage or unacceptable performance drift. They prioritize the structural and material robustness of the filter itself (capacitors, inductors, insulation systems, creepage/clearance, thermal design), but they do not necessarily mitigate the transient for downstream circuits.
- 2. EMI Filters with Transient Suppression** limit, divert, or absorb transient energy to protect downstream circuits. They typically employ protection elements, such as Metal Oxide Varistors (MOV), Transient Voltage Suppression (TVS) diodes, Gas Discharge Tubes (GDT), or combinations of these devices in more sophisticated networks.

These objectives are complementary but not equivalent. Traditional EMI filters while designed to survive a transient event without damage may pass significant transient energy downstream. An EMI filter with built-in suppression will significantly reduce transient energy being passed through to the device or system. Adding transient suppression increases cost, physical size (often significantly), and the possible destruction of protective devices if exposed to higher-than-expected transient energy or a higher number of transient events. Selecting the right approach depends on mission reliability, system architecture, standards compliance, expected transient severity and repetition, maintenance strategy, and cost/size constraints.

This white paper clarifies the difference between traditional EMI filters and EMI filters with transient suppression and classifies transient energy levels and standards. It itemizes design details, cost and size factors, and performance variations for various suppression components.

1. Introduction: EMI Filters and Transient Suppression

Traditional EMI filters without transient suppression are designed to attenuate unintended emissions from electronic devices and prevent external EMI from disrupting electronic devices. The actual EMI levels are often significantly lower in amplitude than the intended electrical signals present, i.e. conducted emissions on 120Vac power lines are often limited by regulation to levels below 0.001Vac. EMI filters provide attenuation to these relatively low-level and continuous disturbances by frequency selection. They allow the required frequencies to pass through with minimal loss but provide high attenuation to unwanted interference frequencies.

Transient suppression devices are amplitude selective rather than frequency selective. They allow signals at or below the required amplitude level to pass through with minimal loss but provide attenuation to unwanted high-voltage transients. In the case of the 120Vac power line referenced above, expected transient environments can be 1000 volts or more.

Not all devices or systems that have transient requirements need an EMI filter with built-in transient suppression. Some devices may possess inherent transient immunity or may already have transient suppression devices in place. These devices would only require traditional EMI filters that can survive the expected transient environment without damage.



Two EMI filters. There is no way to tell by looking at them if they have built-in transient suppression.

In real environments, transient events are generated with widely varying voltage, current, rise time, duration, and repetition rates.

- **Fast, low-energy transients** (tens of nanoseconds to microseconds) from electrostatic discharge events, relay coil de-energization and switching spikes. These transients often have high peak voltages but typically have lower current levels and low duration times so the total energy is relatively low. Traditional EMI filters are often effective at suppressing these types of transients without the need for dedicated suppression components.
- **Moderate-energy transients** (microseconds to milliseconds) from lightning effects, motor contactor operations, or power grid switching. Some of these transients have lower voltages than the low-energy spikes listed above, but with longer durations and increased current levels traditional EMI filters without suppression components provide little to no transient suppression.
- **High-energy transients** (microseconds to seconds) from lightning coupling, HEMP, and vehicular system load dumps when alternators suddenly disconnect from batteries or large inductive loads drop off the bus. Suppression of these types of transients typically requires high-powered devices that are engineered to specific system requirements. The suppression components are likely to be significantly larger than a traditional EMI filter.

2. Transient and EMI Filtering Basics

2.1 EMI Filter Topologies

EMI filters attenuate undesired signals via reactive impedance shaping. Typical topologies include:

- **Single-Stage LC** (series inductor, shunt capacitor): Good baseline attenuation for differential-mode noise.
- **Two-Stage LC or π -Filter** (C-L-C or L-C-L): Improved high-frequency roll-off; can be tuned for specific emission bands.
- **Common-Mode Chokes (CMC)**: Cancel common-mode currents when installed in twisted pair and power input circuits. Whole cable ferrite beads commonly used on I/O cables internally and externally to equipment chassis.
- **X Capacitors** (line-to-line) and **Y Capacitors** (line-to-ground): Provide shunt paths for noise; safety ratings are crucial (Class X1/X2 and Y1/Y2).

These components are selected to provide insertion loss over the spectrum of interest.

However, under transient stress, component voltage ratings, surge current capability, dielectric robustness, and mechanical integrity dominate.

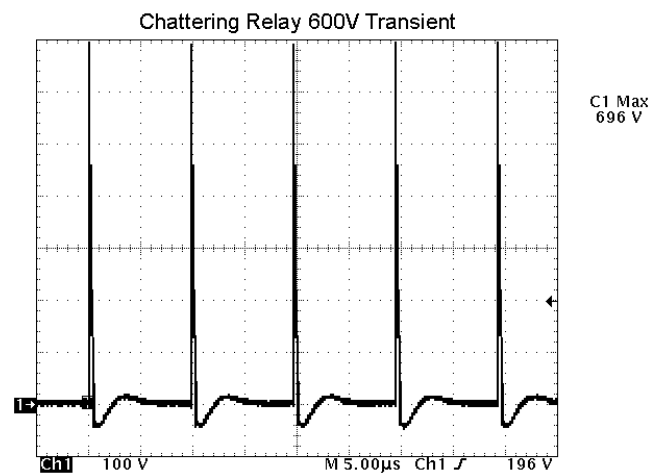
2.2 The Nature of Transients

A transient can be characterized by peak voltage V_{pk} , peak current I_{pk} , rise time t_r , duration t_d , energy E , and repetition rate f_{rep} . The energy is:

$$E = \int_0^{t_d} v(t) i(t) dt$$

While EMI attenuation is mostly a frequency-domain design, transient interaction is time-domain, and device stress is determined by both instantaneous power $p(t) = v(t)i(t)$ and thermal diffusion within the component.

- **ESD**: Very fast rise time (less than a nanosecond), and 100 nanosecond duration, high peak current, modest energy. TVS diodes excel due to ultra-fast response. Traditional EMI filters with larger capacitance values provide effective suppression.
- **Switching Transients (Chattering Relay and IEC 61000-4-4)**: Nanoseconds to microseconds, repetitive. TVS diodes and traditional EMI filters provide effective suppression.
- **Surge (lightning coupling, IEC 61000-4-5 waveforms, MIL-STD-461 CS116/CS117)**: Microsecond rise, tens to hundreds of amps, higher energy. MOVs and GDTs provide energy handling and high current diversion. Traditional EMI filters are ineffective and transient suppression devices are usually required.
- **HEMP Transients**: The initial E1 pulse has a nanosecond rise time, over 50,000 volts and 2500-5000A, and the E2 Pulse is like a lightning surge. Requires large transient suppression devices AND large purpose-built EMI filters.



The 600V “Chattering Relay” test has been around since the 1960’s. It is still called out in RTCA/DO-160 Section 19.

Understanding the transient requirements drives the choice between suppression vs. survivability in the EMI stage.



A 16A EMI powerline filter with transient suppression designed to meet MIL-STD-188-1 for HEMP protection. The entire filter would be about 80% smaller without the transient suppression.

3. Definitions Refined: Suppression vs. Survivability

3.1 Transient Suppression

Objective: Protect downstream circuitry by limiting voltage, shaping waveforms, or diverting current during a transient.

Transient Suppression Components

- **MOVs (Metal-Oxide Varistors):** Voltage-dependent resistors that clamp at a characteristic knee. High energy capability, slower than TVS, degrade with repeated surges (grain boundary aging). A traditional EMI filter with MOV suppression can often match the response time of TVS suppression and at higher energy levels.
- **TVS Diodes (Avalanche/Zener):** Fast response (picoseconds to nanoseconds), low clamping voltage spread, lower energy than MOVs, can be coordinated in arrays to manage line and ground paths.
- **GDTs (Gas Discharge Tubes):** Trigger at high voltages to divert large currents to ground; very high energy handling but have delay and require follow-current management, often achieved with traditional EMI filtering.



This is the ubiquitous 20mm powerline Metal Oxide Varistor used in AC and DC power circuits.



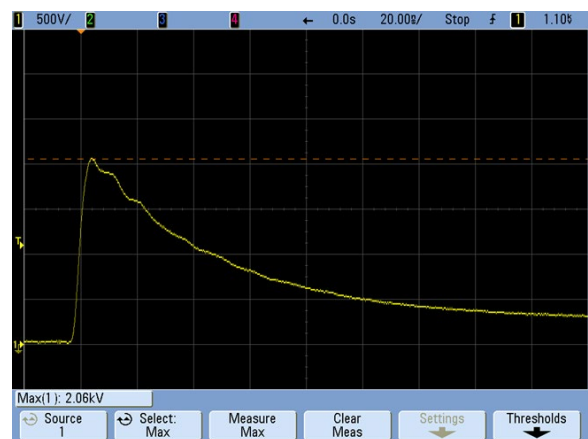
The Gas Discharge Tube is not well suited to powerline applications like an MOV, because once it has been activated by a Surge it will continue conducting usually causing catastrophic failure. Standard practice is to put an MOV in series with the GDT. This eliminates the leakage current from the MOV, stops follow-on current in the GDT, extends component life, and improves the Surge capacity of the circuit.



Transient Voltage Suppression diodes don't handle as much energy but turn on faster than MOVs and GDTs. They work especially well for ICs and I/O line protection and are more common in low-voltage DC circuits than GDTs and MOVs.

Behavior and Risks

- **Aging/Degradation:** MOVs absorb energy and gradually shift clamping characteristics; TVS can fail short/open under extreme events.
- **Thermal Management:** Requires heat sinking, spacing, and fire-safe encapsulation.
- **Interaction with EMI attenuation:** Added suppression devices affect filter resonance points.



2kV EFT Waveform applicable for Commercial and Industrial equipment. Many traditional EMI filters will survive and suppress without additional transient suppression components.

3.2 Transient Survivability

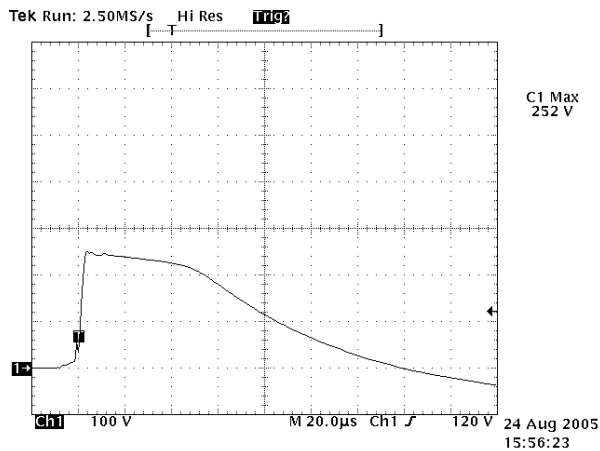
Objective: Ensure the filter itself remains operational and safe post-transient, even if the transient passes through.

Characteristic Design Features

- **High-Surge-Rated Capacitors:** Metallized film or safety ceramics with high dv/dt tolerance; X/Y safety classes, reinforced dielectric thickness.
- **Inductor/CMC Robustness:** Wire gauge and insulation systems selected to withstand thermal stress and avoid core saturation under transient current.
- **Creepage/Clearance and Insulation:** Enlarged spacing or potting to prevent arcing.

Behavior and Trade-offs

- **No Intentional Clamping:** Downstream remains exposed unless external suppression exists.
- **Stable EMI Performance:** Since no clamp devices are included, the filter's frequency response remains consistent over life.
- **Weight/Size Increase:** Survivability features can increase form factor (larger capacitors, thicker insulation, greater spacing).



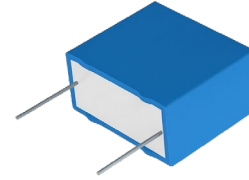
This is what the 2kV Surge waveform looks like after it passes through an EMI filter with transient suppression. The 2kV peak has been reduced to 252 volts after the filter.

4. Component-Level Engineering

4.1 Capacitors: X & Y Safety Classes, dv/dt , and Pulse Life

- **X Capacitors (line-to-line):** Usually Class X2 ($\leq 300\text{--}350$ VAC for typical mains environments) or X1 (higher surge). For survivability, metallized polypropylene offers self-healing; film thickness and metallization patterns are tuned for pulse endurance.

- **Y Capacitors (line-to-ground):** Y1/Y2 classes are mandated to ensure user safety under fault conditions. Transients can force high displacement currents to ground; survivability requires dielectric systems that resist puncture. In suppression filters, Y capacitors are sometimes complemented with GDTs or TVS to limit line-to-ground overvoltage.



Film X and Y safety capacitors are designed to survive high-voltage transients.

4.2 Suppression Devices: MOV, TVS, GDT Selection

MOVs

- **Clamping voltage V_C :** Select so that protected electronics stay below absolute maximum ratings, with margin for tolerance and temperature.
- **Joule rating:** Based on expected surge energy profile. Multiply by safety factor for repeated exposure.
- **Aging:** Define maintenance intervals or monitoring (thermal sensors/thermal fuses).

TVS Diodes

- **Stand-off voltage V_{WM} :** Must exceed normal operating voltage with margin.
- **Clamping V_C at a specified current pulse (e.g., 8/20 μs).**
- **Dynamic resistance R_d :** Determines how the clamp scales with current; lower is better for tight clamp.

GDTs

- **Breakdown voltage V_{BD} :** Set above nominal line but below insulation capability of downstream when a surge occurs.
- **Follow-current behavior:** Use series impedance to prevent sustained conduction. GDTs excel at high energy but are slower to fire; pair with MOV/TVS for fast pre-clamping.

Coordination Strategy

- TVS for ultra-fast edges and low energy.
- MOV for bulk energy absorption.
- GDT for very high surge diversion to ground.
- Series inductance/resistance to shape di/dt and limit follow-current.

5. EMI Performance vs. Transient Behavior

Adding suppression devices alters impedance at transient frequencies. For example, a MOV introduces non-linear resistance that can dampen a filter resonance or, conversely, create a new resonance with line inductance. Survivability-only filters preserve EMI performance across life, improving compliance stability.

6. Standards Landscape: What Each Emphasizes

While this paper avoids reproducing specific standard text, the following themes guide design goals:

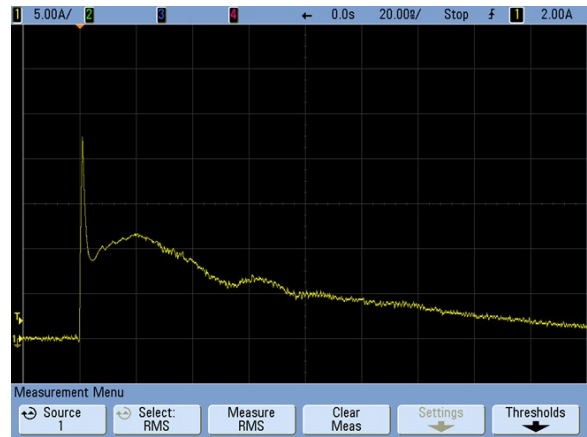
Transient Suppression-Oriented Standards

- **IEC 61000-4-2 (ESD):** Device must not malfunction under electrostatic discharges; fast clamping elements are key.
- **IEC 61000-4-4 (EFT):** Equipment must survive specified EFT levels with acceptable performance; traditional EMI filters typically required for compliance.
- **IEC 61000-4-5 (Surge):** Equipment must survive specified surge waveforms with acceptable performance; suppression devices typically required to meet voltage limits.
- **MIL-STD-461 CS116/CS117 (Damped sinusoidal transients/lightning):** Equipment must survive specified transients with acceptable performance; traditional EMI filters and Transient Suppression typically required for compliance.
- **RTCA/DO-160 Section 22 (Lightning induced transients):** Equipment must survive specified transients with acceptable performance; traditional EMI filters and Transient Suppression typically required for compliance.
- **MIL-STD-188-125 (HEMP):** Equipment must survive specified transients with acceptable performance; EMI filters with transient suppression required for compliance.
- **UL 1449:** Safety for surge protective devices (SPD); focuses on MOV-based devices and thermal safety.

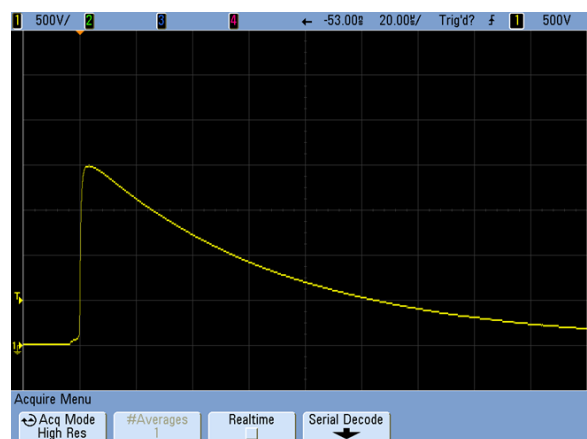
7. Design Trade-Offs and Lifecycle Considerations

7.1 Cost, Size, and Thermal Budget

- **EMI Filters with Transient Suppression:** Added MOV/TVS/GDT increases BOM cost and component size. Thermal fusing or monitoring can add complexity.
- **Traditional EMI Filters:** Larger capacitors, better insulation, bigger inductors. Up-front cost may be higher but replaces consumable elements with durable ones.



IEC 1000-4-2 and MIL-STD-461 CS118 6kV ESD current waveform – Deadly to modern ICs, but many traditional EMI filters can survive without transient suppression.



The 2kV IEC 1000-4-5 waveform is a std. test requirement, typically suppressed using MOVs inside the powerline filter or as an external replaceable component.

7.2 Reliability and Maintenance

- **EMI filters with Transient Suppression Aging:** MOVs change clamp characteristics over time; schedule preventive replacement in surge-heavy environments. TVS arrays in high duty ripple can exhibit leakage increase.
- **Traditional EMI Filters Longevity:** Passive components age mostly by environmental stress (temperature, humidity) rather than consumption by surges. EMI performance tends to be stable across life.

7.3 Safety and Failure Modes

- **EMI Filters with transient Suppression Risk:** Catastrophic failure of MOVs under sustained over-voltage may cause thermal runaway; use thermal disconnects and flame-retardant materials.
- **Traditional EMI Filters Risk:** While filter remains intact, downstream may be exposed. Mitigate at system level: SPD upstream, localized clamps, surge-rated semiconductors.

8. Practical Design Methodology

8.1 Requirements Capture

- **Define Transient Environment:** Voltage levels, current, waveform (rise time/duration), repetition, and coupling mode (line-to-line, line-to-ground, common-mode).
- **Downstream Limits:** Absolute maximum ratings of semiconductors, isolation boundaries, creepage/clearance constraints, and safety leakage limits.
- **Compliance Standards:** Which standards apply? Are suppression behaviors mandated or only survivability?
- **Maintenance Strategy:** Is periodic inspection feasible? Are field replacements easy?

8.2 Component Sizing Calculations

Example for MOV selection:

- **Operating voltage:** 120 VAC RMS \rightarrow peak \sim 170 V.
- **Select MOV nominal V_{nom}** around 275–320 VAC rating to avoid conduction in normal operation.
- **Clamping at surge current:** Ensure $V_c <$ downstream maximum (e.g., 600 V for bridge rectifier) with margin.
- **Energy:** Calculate expected energy from surge test. Choose MOV with $\geq 2\times$ energy rating and validate.

Inductor/CMC

- Determine worst-case surge current I_{surge} .
- Select core and wire such that $I_{sat} > I_{surge}$ and temperature rise stays within insulation class.
- Consider transient mechanical forces (Lorentz forces on windings) for potting and tie-down.

Capacitors

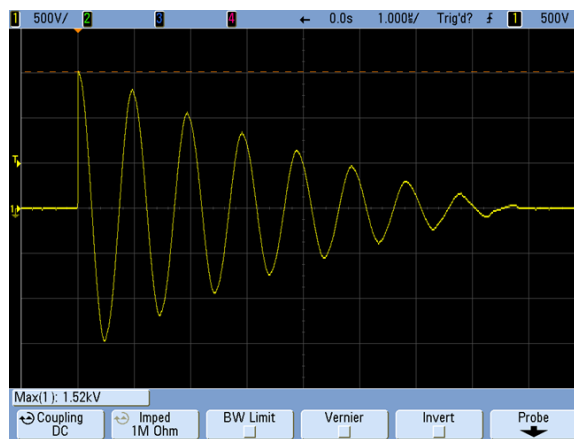
- Determine maximum current from dv/dt ; ensure pulse capability (datasheet curves).
- Verify surge voltage vs. dielectric thickness and safety class.

8.3 Layout and Packaging

- **Short, Wide Traces:** Reduce inductance to improve TVS/MOV effectiveness.
- **Star Grounding for Clamps:** Provide low-impedance path to chassis; avoid sharing return paths with sensitive signals.
- **Creepage and Clearance:** Respect environment (pollution degree, over-voltage category).
- **Thermal Spacing:** Keep MOVs away from heat-sensitive components; provide airflow or heatsinking.

8.4 Validation and Test

- **Pre-Compliance:** Use surge generators (e.g., 1.2/50 μ s & 8/20 μ s), ESD guns, EFT/burst generators, MIL-STD-461 CS116 damped sinusoidal generators.
- **Monitor:** Clamp voltages at critical nodes, downstream device stress, temperature of suppression devices, and filter parametric drift (insertion loss).
- **Pass/Fail Criteria:** For suppression, downstream waveforms must remain within limits; for survivability, the filter must not degrade or fail and functional operation must continue.



The 1MHz Damped Sinusoidal Transient called out in MIL-STD-461 CS117 and RTCA/DO-160 Section 22. Traditional EMI Filters designed for power lines typically have high suppression of the 1MHz and 10MHz waveforms. Low-capacitance (less than 10nF) filters like those used in 38999 Circular connectors and other I/O connectors must be designed with high-dielectric thickness ceramics for survivability.

9. Procurement and Qualification Guidance

9.1 Specifying EMI Filters with Transient Suppression

- **Clamp Voltage Windows:** Specify max clamp voltages under defined surge currents; include temperature extremes.
- **Energy and Repetition Ratings:** Require demonstrated survival over N surges with no catastrophic failure and minimal drift.
- **Thermal Disconnects:** For MOVs, specify integrated thermal fuses or protection mechanisms.
- **Compliance Evidence:** Request test reports (IEC surge/ESD) showing waveforms and clamp performance at the DUT terminals.

9.2 Specifying Traditional EMI Filters

- **Surge Withstand Voltage:** Define exposure waveforms and pass/fail criteria focusing on no damage, continued operation, and parameter stability (e.g., insertion loss within $\pm x$ dB).
- **Creepage/Clearance and Insulation:** Ensure over-voltage category, pollution degree, and altitude are met; call out coating/potting requirements.
- **Environmental Durability:** Vibration, humidity, and thermal cycling appropriate for the application.

10. Engineering Checklists

10.1 Suppression Filter Design Checklist

- Identify transient waveforms and repetition.
- Choose MOV/TVS/GDT clamps with appropriate clamp voltages and energy ratings.
- Add series impedance to control di/dt and follow-current.
- Verify thermal path and include thermal disconnect for MOV banks.
- Simulate non-linear behavior; measure clamp at DUT terminals.
- Confirm EMI performance does not degrade unacceptably.
- Define maintenance intervals and replacement criteria.

10.2 Survivability Filter Design Checklist

- Select surge-rated X/Y capacitors for power input lines
- Size inductors/CMC for I_{sat} and thermal robustness under surge.
- Ensure creepage/clearance and insulation meet environment.
- Consider potting/conformal coating for arc resistance and vibration.
- Validate with ESD/EFT/Surge/Lightning exposure: no damage, stable insertion loss.
- Plan external SPD and localized clamps for downstream protection.
- Document inspection and re-qualification steps.

11. Conclusion

Traditional EMI filters designed for transient survivability and EMI filters with transient suppression reflect two distinct engineering priorities. EMI filters with transient suppression interact with transient energy to protect downstream circuitry, integrating clamp devices like MOVs, TVS diodes, and GDTs. They provide immediate load protection but introduce issues of aging, thermal management, and potential shifts in EMI characteristics. Traditional EMI filters designed for transient survivability withstand transients without

failing, prioritizing robust passives, insulation spacing, and mechanical integrity to preserve operational continuity and stable EMI performance. However, they do not guarantee the downstream will remain within its safe operating limits during severe events.

Choosing between them requires a nuanced appreciation of the environment, compliance obligations, lifecycle expectations, failure modes, and maintenance realities. In mission-critical systems (aerospace/defense, medical), survivability is often paramount—paired with system-level suppression that is serviceable and certifiable. In industrial/commercial systems with frequent electrical disturbances, integrating suppression into the EMI filter may reduce downstream protection requirements and simplify deployment—provided maintenance is planned, and thermal safety is ensured.

For many applications, the optimal solution is hybrid: a survivable EMI filter at the point of entry, serviceable surge protection devices to handle bulk energy, and localized fast clamps near sensitive loads. This architecture aligns with robust reliability, predictable compliance, and manageable maintenance—ultimately delivering equipment that both keeps operating through transients and protects what matters behind the filter.